Name: Dr. Somenath Dutta

Designation: Assistant Professor

Branch: Electronics & Communication Engineering

Educational Qualification(s):

Qualification(s)	**University**
B.Tech	W.B.U.T
M.Tech	W.B.U.T
Ph.D	IIT (ISM) Dhanbad

Experience in years : More than 9 Years

Academic:8 years 6 months

Details:

Sl. No.	Organization	Position Held	Duration	
			From	To
1	RVS College of Engineering &Technology Jamshedpur, Jharkhand.	Assistant Professor	May 2023	January 2025
2.	Maryland Institute of Technology & Management Jamshedpur, Jharkhand.	Assistant Professor	May 2018	April 2023.
3	BIT Sindri, Dhanbad, Jharkhand	Lecturer	February 2016	December 2017

Industrial: 1 year

Details:

Sl. No.	Organization	Position Held	Duration	
			From	To
1	Jayenn India Pvt. Ltd	Service & Maintenances Engineer	2010	2011

Other Information:

a) **Publication details. (Journals)**

1. Somenath Dutta, Prashant Kumar, Rajeev Kumar Ranjan, Dharmendrakumar Singh, "An Improved DDCCTA toward its Application in Different Wave-Function and PWM Generation". Arabian Journal for Science and Engineering (2022). (SCI-Q2, Impact factor: 2.807) doi.org/10.1007/s13369-022-07559-x.

2. Somenath Dutta, SagarSurendra Prasad, et.al, "Floating/Grounded Memristor Model Employing Single DDCCTA with Resistor Tunability", International Journal of Electronics, Taylor & Francis. (SCI-Q3, Impact factor: 1.54) doi.org/10.1080/00207217.2023.2267218.

3. Niranjan Raj, Somenath Dutta, et.al, "VDTA Based Transadmittance Mode Instrumentation Amplifier with Experimental results." International Journal of Electronics, Taylor & Francis. (SCI-Q3, Impact factor: 1.54) doi.org/10.1080/00207217.2024.2439182.

4. KamanashisGoswami, HaraprasadMondal, Somenath Dutta, "Design and analysis of 1:2 line optical decoder based on linear optics." Elsevier's e-Prime - Advances in Electrical Engineering, Electronics and Energy. (SCIE, Impact factor: 0.88) doi.org/10.1016/j.prime.2023.100190.

5. HaraprasadMondal, Somenath Dutta, et.al, "Design and simulation of phase shifter based on multimode interference in photonic crystal waveguide." The European Physical Journal D. SCIE. doi.org/10.1140/epjd/s10053-023-00768-5.

(b) Details of Seminar/Workshop/Conference.

1. Somenath Dutta, Rajeev Kumar Ranjan, D.K. Singh, "Designing Tunable Circuits Using the DDCC2TA for Square, Triangular Wave Generation and Pulse Width Modulation". Accepted for publication in Second International Conference on Electrical, Electronics, Information, and Communication Technologies (ICEEICT 2023). DOI: 10.1109/ICEEICT56924.2023.10157604

2. Somenath Dutta et. al., "Operational Transconductance Amplifier (OTA) based dual mode Multiphase Oscillator". Accepted for publication in 5th IEEE International Conference on Recent Trends in Computer Science and Technology (ICRTCST-2024) held on 15th and 16th April 2024 at RVSCET, Jamshedpur, Jharkhand (India) DOI: 10.1109/ICRTCST61793.2024.10578383.

3. S. Dutta et. al., "Design of Efficient Photonic Coupler Structures for Lumped Raman Amplification in Silicon Waveguides", in International Conference on Devices, Circuits & Communications, BIT Mesra (IEEE) DOI: 10.1109/ICDCCom.2014.7024712.